

2. In Claim 32, lines 29-30, the wording "conductor elements so that said horizontal stripe conductor elements are electrically connected to each other" has been replaced by "conductor elements".
3. In Claim 32, lines 34-35, the wording "on laterally opposite ends" has been replaced by: "at laterally opposite ends".
4. In Claim 33, line 2, the wording: "with the associated connected first semiconductor layer" has been replaced by: "with the first semiconductor layer".
5. In Claim 33, line 4, the wording "and associated with said plurality" has been replaced by "together with said plurality".
6. In Claim 35, line 4, the wording "input/output pin" has been replaced by "input pin or output pin".
7. In Claim 49, line 11, the wording: "layer having a top element making" has been replaced by ", wherein a top element makes".
8. In Claim 49, lines 24-25, the wording "conductor elements so that said horizontal stripe conductor elements are electrically connected to each other" has been replaced by: "conductor elements".
9. In Claim 49, line <sup>28</sup>~~29~~, the wording "second one of said first +-type regions" has been replaced by "second one of said first n+ - type regions".
10. In Claim 49, line 29, the wording: "on laterally opposite ends" has been replaced by "at laterally opposite ends".

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